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## APPLICANTS

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*MW/SA* \*\* CONTINUING DATA \*\*\*\*\**yes/SA* \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-227685 07/27/2000

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	3	1420	24
Verified and Acknowledged	<i>E.Sau</i> Examiner's Signature <i>SA</i> Initials				

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## TITLE

Semiconductor memory device having ECC type error recovery circuit

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